



云智科技
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Scan board Specification

VCS200

Version: V01 Date: 2017-07-31

Board Dimensions

VCS200 is designed for rental screen, light bar screen, shaped screen, light source and color screen design and development of a product. Stable and reliable performance, and its high refresh rate, high gray level features a full-fidelity image, retaining the best display screen. Support secure online upgrade, and tracing information preservation, on the use of more secure, and quick.

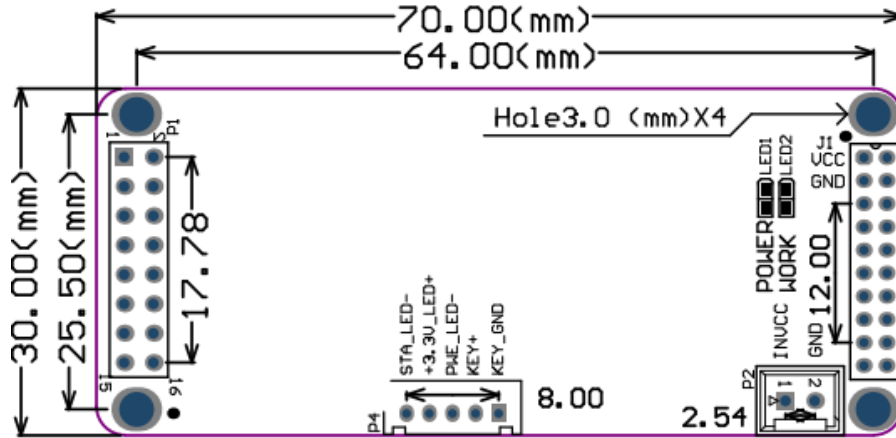
Product Feature

- Operating voltage: 5V DC;
- Load points: 64*64;
- High refresh rate, high gray level;
- Professional lighting panel;
- Dimensions: 70 x 30 (mm * mm);

Technical Specifications

| | | | |
|---------------------|---|---------------------|--------------------------------------|
| Load area | In 64*64, Any trace point | Online Upgrade | Support |
| Refresh rate | Static screen up above 5500Hz | Number of outputs | 16 groups of data |
| Interface Type | 2*10PIN, HUB board output | data groups | |
| Scanning mode | Static -4 scan | Cascade card number | Generally the value 256, maximum 512 |
| Gray levels | 4096 — 65536 | Loss of brightness | 5%-20% |
| By-point correction | brightness, color | Operating Voltage | 5V DC |
| Chip Support | Conventional chip, PWM chips, lighting chip | Working temperature | 0℃-60℃ |
| Dimensions | Length 70 * width 30 (mm) | | |

Board Dimensions



Interface Definition

1) J1 output interface definition

| J1 | | | |
|------|----|----|------|
| VCC | 1 | 2 | VCC |
| GND | 3 | 4 | GND |
| DO2 | 5 | 6 | DO1 |
| DO4 | 7 | 8 | DO3 |
| DO6 | 9 | 10 | DO5 |
| DO8 | 11 | 12 | DO7 |
| DO10 | 13 | 14 | DO9 |
| GO12 | 15 | 16 | GO11 |
| DO14 | 17 | 18 | DO13 |
| DO16 | 19 | 20 | DO15 |

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2) P4 output interface definition

| P4 | |
|----------|---|
| GND | 1 |
| SW | 2 |
| PWR_LED- | 3 |
| VC33V | 4 |
| STA_LED | 5 |

Header 5H

3) P1 output interface definition

| P1 | | | |
|----------|----|----|----------|
| P1_TX0_N | 1 | 2 | P1_TX0_P |
| P1_TX1_N | 3 | 4 | P1_TX1_P |
| P1_TX2_N | 5 | 6 | P1_TX2_P |
| P1_TX3_N | 7 | 8 | P1_TX3_P |
| P0_TX0_N | 9 | 10 | P0_TX0_P |
| P0_TX1_N | 11 | 12 | P0_TX1_P |
| P0_TX2_N | 13 | 14 | P0_TX2_P |
| P0_TX3_N | 15 | 16 | P0_TX3_P |

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